

# Zhi Li

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## Education

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| 08/2017-05/2019 | <b>University of Florida (UF), Gainesville, FL, US</b><br>M.Sc. in Electrical and Computer Engineering<br>GPA: 3.83                |
| 09/2012-06/2016 | <b>Dalian University of Technology (DUT), Dalian, Liaoning, China</b><br>B.Eng. in Integrated Circuit Design and Integrated System |

## Publication

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- Li X, Li X, **Li Z**, et al. *WS<sub>2</sub> nanoflakes based selective ammonia sensors at room temperature*[J]. Sensors and Actuators B: Chemical, 2017, 240: 273-277. 03/2017

## Honor

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| 1 <sup>st</sup> Prize, Provincial Award of Natural Science Academic Achievement, Liaoning Province | 09/2018 |
| Member of Interdisciplinary Microsystems Group (IMG) at UF   | 01/2018 |
| Outstanding Graduate and won the <i>Excellent Graduation Design</i> at DUT                         | 03/2016 |

## Research

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| <b>Titanium Dioxide (TiO<sub>2</sub>) Nanoparticles dispersal Research</b>   | 11/2018-Present |
| <b>Mentor:</b> Huikai Xie, Professor (UF)  |                 |
| <ul style="list-style-type: none"> <li>✧ Measuring the refractive index and attenuation coefficient for TiO<sub>2</sub> aqueous solution</li> <li>✧ Utilizing the TiO<sub>2</sub> nanoparticles solution to improve performance of OCT probe (immersed OCT probe)</li> </ul>   |                 |
| <b>Deep Learning of Optical Coherence Tomography (OCT) System</b>  | 08/2018-Present |
| <b>Mentor:</b> Huikai Xie, Professor (UF)  |                 |
| <ul style="list-style-type: none"> <li>✧ Optimizing the structure of endoscopic OCT probe and miniaturization</li> <li>✧ Exploring various type of MEMS used for endoscopic OCT probe testing</li> <li>✧ Doing parallel research of Two-Photon Microscope (TPM) probe</li> </ul>   |                 |
| <b>OCT Probe Structure Design and Evaluating</b>   | 03/2018-05/2018 |
| <b>Mentor:</b> Huikai Xie, Professor (UF)  |                 |
| <ul style="list-style-type: none"> <li>✧ Designed OCT probe structure and assembly assistant parts based on AutoCAD and SolidWorks software</li> <li>✧ Finished OCT probe assembly independently</li> </ul>  |                 |
| <b>Optimized Sensitivity for Layered (Mo, W) S<sub>2</sub> Nanoflakes VOCs Sensor</b>  | 03/2016-06/2016 |
| <b>Mentor:</b> Xiaogan Li, Professor (DUT)   |                 |
| <ul style="list-style-type: none"> <li>✧ Processed and explored (Mo, W) S<sub>2</sub> material with SEM, TEM, XRD and XPS</li> <li>✧ Achieved gas-sensitivity and photosensitivity test of (Mo, W) S<sub>2</sub> sensor, and mechanism analysis</li> <li>✧ Optimized sensitivity for (Mo, W) S<sub>2</sub> sensor with photon energy (LED illumination)</li> </ul> |                 |
| <b>MoS<sub>2</sub> Stripping Procedure and Gas-Sensitivity Research</b>  | 03/2015-02/2016 |
| <b>Mentor:</b> Xiaogan Li, Professor (DUT)   |                 |
| <ul style="list-style-type: none"> <li>✧ Obtained single and monolayer MoS<sub>2</sub> by grinding raw material</li> <li>✧ Tested the selectivity and gas-sensitivity capability of MoS<sub>2</sub> sensor for different relative humidity</li> <li>✧ Concluded relationship between resistance change and specific gas environment</li> </ul>                     |                 |

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**Digital Integrated Circuit Design EDA**

07/2015

**My responsibilities:**

- ✧ Designed schematic diagram and graphical diagram
- ✧ Conducted layout design and PEX with Calibre
- ✧ Completed simulation and layout by DRC and LVS in the end

**Programming Design Based on MCU Assembly Language**

04-05/2015

**My responsibilities:**

- ✧ Adopted the  $\mu$ virson2 of KEIL integrated debugging software to develop programs
- ✧ Conducted the Single Chip Micropyco I/O port experiment, ADC module TLC549 programming design, digital display and clock system design based on ZLG7290B & PCF8563T

**Electronic System Simulation Design Based on Multisim12.0 Software**

10-11/2014

**My responsibilities:**

- ✧ Calculated the critical parameter of differential-mode voltage ratio, common-mode rejection ratio (CMRR), and Signal-Noise Ratio (SNR)
- ✧ Designed electric circuit simulation and conducted transient, noise and waveform analysis

**Integrated Development Environment Design Based on VHDL Language**

03-04/2014

**My responsibilities:**

- ✧ Developed digital clock with DE2 development board under PC386 environment
- ✧ Used QUARTUS II based on the VHDL to implement the functions of time display, timing, alarm clock

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**Internship**

Dalian Semiconductor Technology Institute, China

07-08/2015

**Content:** Learned the wafer manufacturing and the basic wafer processing technology such as vacuum sputtering, magnetron sputtering, photolithography, etching and cleaning; visited and mastered the clean room operation procedures.

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**Proficiency**

COMSOL, AutoCAD, SolidWorks, Inventor, Multisim 12.0, QUARTUS II, KEIL, OriginPro 2016, HSPICE, LabVIEW, VMware, Cadence, etc.