ONKAR SHRINIVAS BHENDE

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EDUCATION

Master of Science in Electrical and Computer Engineering, GPA: 3.26, Aug 2010-May 2012

University of Florida (UF), Gainesville, FL, USA,

Concentration in Semiconductor Devices

Bachelor of Technology in Electronics Engineering, GPA: 6.89(10.0), July 2004-May 2008

Sardar Vallabhbhai National Institute of Technology (SVNIT), Surat, India

Active member of the technical hobby club 'DRISHTI' including participations in events like Robo-Sumo and Sky-Bridge.

PROFESSIONAL EXPERIENCE

Intern at Wafer Technology & Foundry Organization, NXP Semiconductors, Nijmegen, Netherlands, Sep 2011-Jan 2012

- Aim was to develop a new mathematical model for overlay in case of wafer scale products (1 die/wafer) with the help of lithography and metrology team.
- Responsibilities included definition of experiment setup, guidance of wafer during process flow, measurements (done manually inside the cleanroom), and analysis of the results.
- Successfully developed a new software program (using VBA/Excel macros) to extract the desired information from experiment raw data.

Systems Engineer at Infosys Technologies Limited, Pune, India, Sep 2008-June 2010

- Scored 5.0/5.0 in the initial generic training cycle (Software life cycle, OOPS, C/C++, SQL, UNIX) followed by specialized training in JAVA and SAP technologies.
- Worked on a development/upgrade project with the SAP team of Price Waterhouse Coopers (PWC), USA.
- Won Certificate of appreciation from PWC for contribution to the project.

RESEARCH EXPERIENCE

Member of the Interdisciplinary Microsystems Group (IMG) at University of Florida, Aug 2011-Present

• Currently working on fabrication of high frequency RF antennas on a substrate using 3D lithography.

Visiting Student at Raman Research Institute (RRI), Bangalore, India, Summer 2007

Develop a C program to configure FPGA (Field-Programmable Gate Array) using USB port.

TECHNICAL SKILLS

- Hardware languages: Assembly language, VHDL/Verilog programming using XILINX ISE, Modelsim
- **Circuit Design:** Cadence, SPICE, Agilent ADS (for RF circuits)
- Data Analysis and Numerical Modeling: COMSOL Multiphysics, MATLAB

COMPUTER SKILLS

- Programming Languages: C/C++, HTML, JAVA, SAP (ABAP)
- Databases: Oracle, PL/SQL
- Software Packages: Eclipse, Microsoft Visual Studio, MS Excel macros (VBA)
- Operating Systems: Unix/Linux, Windows

COURSE PROJECTS

Analysis of charge transport in semiconductor by Monte Carlo Simulation at University of Florida

• Wrote term paper on analysis of I-V characteristics of various types of MOSFET's using Monte Carlo simulation.

Circuit design and optimization at University of Florida

- Optimization of MIPS architecture at design level (VHDL) and layout (Cadence)
- Implementation of 1-D time domain convolution on Nallatech board (using C++, VHDL, Dimetalk).

MEMS Design at University of Florida

- Capacitive Proximity Sensor Using MEMS Technology
- Design of MEMS accelerometer for Wii controller

Image Processing (MATLAB) at SVNIT, Surat

• Worked in a team of 5 to develop a tool that would recognize characters on a vehicle number (license) plate.