KEVIN J. POWER

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Current Address
1055 S.W. 62nd Blvd Unit 512
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OBJECTIVE:

A starting position in the field of electrical engineering, specifically in computer hardware and micro-electronic/RF design.

EDUCATION

Bachelor of Science in Computer Engineering – Hardware, expected graduation **Dec 2011**
University of Florida, Gainesville, Florida
Grade Point Average: **3.39 / 4.00**

Minor in French, expected Dec 2011
University of Florida, Gainesville, Florida
Grade Point Average: 3.62 / 4.00 (13 of 16 credit hours received)

EXPERIENCE

Team Leader, Oct 2010 – Dec 2010 (Project active Aug 2010 – May 2011)
IPPD undergraduate research project, University of Florida

- Organized a team of engineers with designing a low-loss RF tuner network for MRI coils

Undergraduate Research Assistant, April 2011 - Present
Multidisciplinary Nano and Microsystems (MnM) Lab

- Worked on ZnO nanowire transistor fabrication and testing

CPU Architect, Nov 2010
Digital Design, EEL 4712, University of Florida

- Designed and implemented a complete eight-bit CPU architecture and instruction set
- Coded all project files in **VHDL**

Specialized Courses, Aug 2007 – Present
Major core classes, University of Florida

- Microprocessor Applications
- Digital Design, Digital Logic
- Operating Systems
- Solid State Electronic Devices
- Electronic Circuits 1&2
- Signals and Systems

SKILLS

Programming: LabVIEW, Java, C, **VHDL**, assembly and **MATLAB**
Languages: Intermediate French and beginning Italian

ACTIVITIES

Cycling, June 2008 – Present

- Racing road bikes, emphasizing team communication and collaboration with a common goal
- Maintaining quality performance from my road bike through precise mechanical care
- **Member**, IEEE, 2009 – Present
- **Member**, Team Florida Cycling (University of Florida), 2008 – Present